

深圳市普恩科技有限公司

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TA1003

8 bits Learning Keyboard

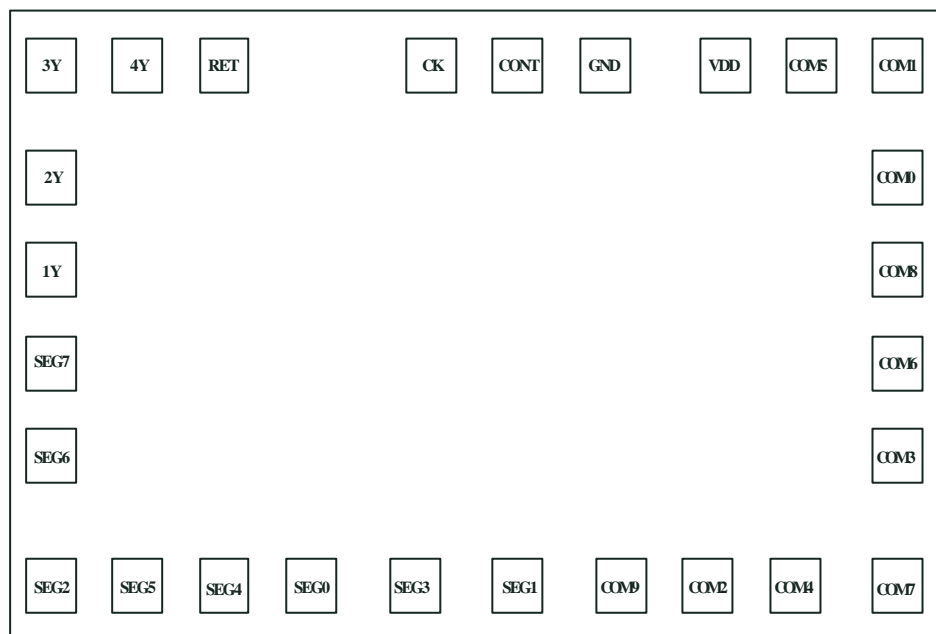
Features :

- Operating Voltage range : 3.5V to 5.5V.
- Low power consumption .

General Description :

This chip is a new type CMOS keyboard controller designed for 8 bit Nintendo, it can be replace the traditional learning keyboard in TV game. this chip can be decoded into 84 keys so you can use this chip in many kinds of applications.

PIN Configuration:



***Substrate to VDD**

Pin Description:

| Pin No | Symbol | Description |
|----------|--------------|------------------------|
| 1 | VDD | Positive power supply. |
| 2 | GND | Negative power supply |
| 3 | CONT | Control input pin. |
| 4 | CK | Clock input pin |
| 5 | RET | Reset input pin |
| 6 to 9 | 1Y to 4Y | Data output pin |
| 10 to 17 | SEG0 to SEG7 | Scan signal input pin |
| 18 to 28 | COM0 to COM9 | Scan signal output pin |

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Absolute Maximum Ratings:

| RATING | VALUE |
|-----------------------|----------------------|
| DC Supply Voltage | <6.5V |
| Input/output Voltage | GND-0.3V to VDD+0.3V |
| Operating Temperature | -10°C to 60 °C |
| Storage Temperature | -25°C to 125°C |

Notice: Stress greater than those listed under Absolute Maximum Ratings may cause permanent damage to the device, This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied, Exposure to absolute maximum rating conditions for extended period may affect reliability.

Electrical Characteristics

(VDD=5.0V,GND=0V,Ta=25°C,unless otherwise specified)

| Parameter | Symbol | Min | Typ | Max | Condition |
|---------------------------|--------|------|------|-------|-----------------|
| Operating Voltage | Vdd | 4.0V | 5.0V | 5.5V | |
| Operating Current | Iop | --- | --- | 600uA | Vdd=5V,I/O open |
| Scan Signal Drive Current | Idrive | --- | --- | 5mA | Vdd=5.0V |
| Scan Signal Sink Current | Isink | --- | --- | 5mA | Vdd=5.0V |

Application Circuit:

